



# 1/4.5-Inch 1.6Mp CMOS Digital Image Sensor

## MT9M002 Data Sheet Addendum

Refer to the MT9M002 data sheet on Micron's Web site: [www.micron.com/imaging](http://www.micron.com/imaging).

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### Introduction

This document supplements Micron's MT9M002 advance data sheet (Revision B) with an update to the typical serial configuration schematic. The standard CMOS digital image sensor data sheet should be referenced for a complete description of this 1/4.5-inch 1.6Mp image sensor. The specifications contained in this addendum supersede the specifications listed in the referenced CMOS digital image sensor data sheet.

### Addendum Changes

This addendum supplements Table 3, "Signal Descriptions," on page 8 and Figure 4: "Typical Configuration – Serial Connection," on page 10 of the MT9M002 Advance data sheet.

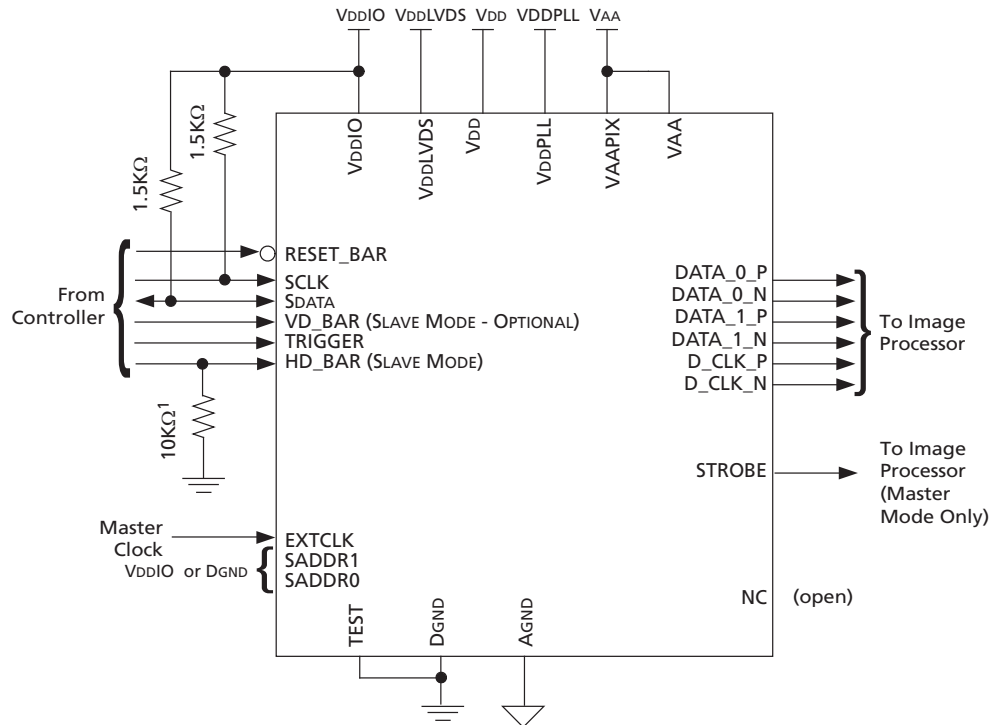


## MT9M002: Serial Interface - Typical Connection Addendum Changes

**Table 1: Signal Descriptions**

Name	Type	Description
SCLK	Input	Serial clock. Connected externally through a resistor to VDD_IO. Typically the resistor value is 1.5KΩ but may vary depending on the system configuration and bus loading.
HD_BAR	I/O	In slave mode, HD_BAR is used as the horizontal sync signal (a required element for slave mode). In master mode the signal becomes active HIGH. Slave mode is selected in default. An external pull-down resistor to DGND (typically 10K–100KΩ) is required for a proper initialization sequence.
SDATA	I/O	Serial data. Connected externally through a resistor to VDD_IO. Typically the resistor value is 1.5KΩ but may vary depending on the system configuration and bus loading.

**Figure 1: Typical Configuration – Serial Connection**



- Notes: 1. 10KΩ–100KΩ pull-down resistor needed if master controller does not drive HD\_BAR during reset.



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**Advance: This data sheet contains initial descriptions of products still under development.**



# MT9M002: Serial Interface - Typical Connection Revision History

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## Revision History

Rev. A .....	5/2/2007
Initial release	