



# 1/2.5-Inch 5-Mp Digital Image Sensor

## MT9P001

### Silicon Revision 8 Errata

## Introduction

This errata summarizes known issues to date for Micron® MT9P001 Silicon Revision 8 (Rev8). This errata is provided for customer information at this time. Any permanent changes will be incorporated into the next revision of the data sheet.

**Table 1: Known Issues and Work Arouns**

Issue #	Title Description Work Around	Details
1	Title	Column shading.
	Description	When operating in binning mode with high gains, column shading becomes visible.
	Work Around	Set reserved register R0x41 to 0x0003.
2	Title	Saturation level.
	Description	At values of analog gain higher than 3X, sensor's saturation level may be incorrect.
	Work Around	Use recommended register settings.

**Table 2: Default Register Changes from Silicon Rev7**

Register Name	Address (Hex)	Default Value (Hex) Rev7	Default Value (Hex) Rev8
Reserved	0x003E	0x0400	0x80C7
Reserved	0x0041	0x0003	0x0000
Reserved	0x0042	0x0005	0x0003
Reserved	0x0043	0x0001	0x0003
Reserved	0x004F	0x0017	0x0014
Reserved	0x0070	0x0067	0x00AC
Reserved	0x0071	0x6404	0xA700
Reserved	0x0072	0x6206	0xA700
Reserved	0x0073	0x1408	0x0C00
Reserved	0x0074	0x160A	0x0600
Reserved	0x0075	0x330C	0x5617
Reserved	0x0076	0x4735	0x6B57
Reserved	0x0077	0x4937	0x6B57
Reserved	0x0078	0x6039	0xA500
Reserved	0x0079	0x6602	0xAB00
Reserved	0x007A	0x6404	0xA904
Reserved	0x007B	0x6206	0xA700
Reserved	0x007C	0x6206	0xA700
Reserved	0x007E	0x6408	0xA900



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**Table 2: Default Register Changes from Silicon Rev7**

Register Name	Address (Hex)	Default Value (Hex) Rev7	Default Value (Hex) Rev8
Reserved	0x007F	0x6404	0xA900



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## Document Change History

Document Version	Date	Change to Document
C	6/29/06	Add Issue #2 to Table 1
B	5/26/06	Add Table 1
A	4/27/06	Initial release