



1/4-Inch 2Mp Digital Image Sensor

MT9D012

Silicon Revision 3 Errata

Refer to the MT9D012 data sheet at www.micron.com/imaging

Introduction

This errata summarizes known issues to date for Micron's MT9D012 silicon revision 3 (Rev3). This errata is provided for customer information at this time. Any permanent changes will be incorporated into the next revision of the data sheet.

Table 1: Known Issues and Work Arouns

Issue #	Title Description Work Around	Details
1	Title	SMIA compression bug
	Description	The MI-2020 Rev3 silicon demonstrates an incorrect behavior when using RAW 10-to-8 bit compression.
	Work Around	This issue does not affect the part operation functionality when in SMIA profile 0.
2	Title	CCM default value
	Description	An improper value was introduced in Rev3 silicon.
	Work Around	This issue does not affect the part or functionality because the ISP will overwrite the original sensor setting during integration. New values provided by the ISP will always take effect.



MT9D012: 1/4-inch 2Mp CMOS Digital Image Sensor Revision History

Revision History

Rev A.....	06/12/2007
• Initial release	



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This data sheet contains minimum and maximum limits specified over the complete power supply and temperature range for production devices. Although considered final, these specifications are subject to change, as further product development and data characterization sometimes occur.