



LVDS AC Specification

MT9M002

Data Sheet Addendum

For more information, refer to the MT9M002 data sheet on Aptina's Web site: www.aplina.com.

Introduction

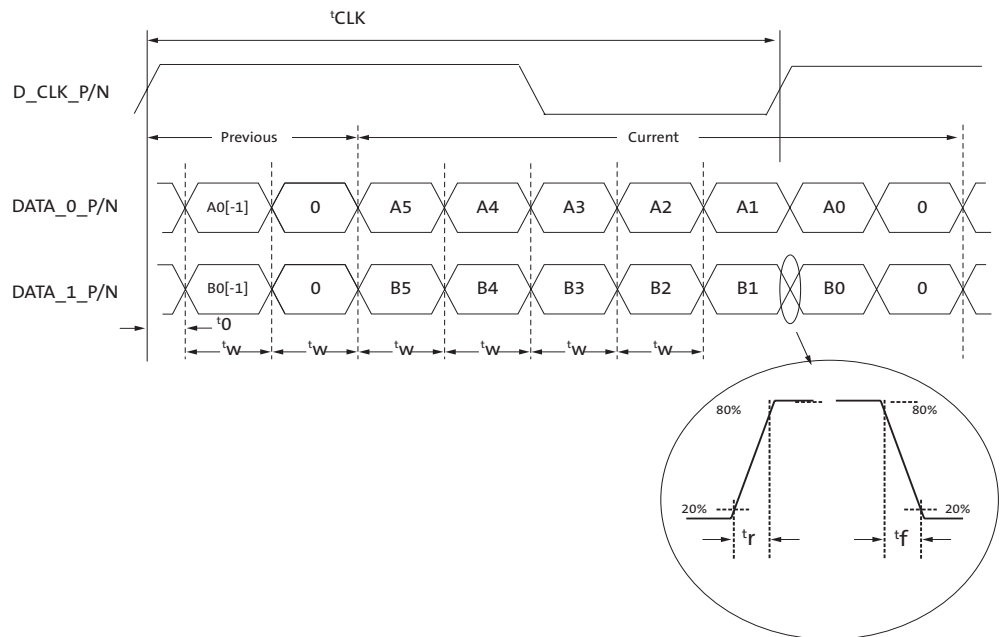
This document supplements Aptina's MT9M002 advance data sheet (Rev. G 3/10) with LVDS AC electrical characteristics. The standard CMOS digital image sensor data sheet should be referenced for a complete description of this 1/4.5-inch 1.6-Mp image sensor. The specifications contained in this addendum supersede the specifications listed in the referenced CMOS digital image sensor data sheet.

Table 1: AC Electrical Characteristics

Symbol	Parameter	Test Conditions ¹	Min	Typ	Max	Unit
t_r	LVDS data rise time	20–80%		230		ps
t_f	LVDS data fall time	80–20%		230		ps
t_0	D_CLK to Bit0 skew	D_CLK frequency is 99 MHz	-30	0	30	ps
t_w	Data bit width		1423	1443	1463	ps

Note: 1. The test results measured in LVDS 14-bit mode, VDDLVDs = 2.6V, Input clock frequency = 10.000000 MHz 50ppm.

Figure 1: AC Waveform and Symbol Definition





Addendum Changes

This addendum supplements the AC electrical specifications of MT9M002.



Revision History

Rev. B	8/10
• Updated to Aptina template	
• Updated to non-confidential	
Rev. A	11/07
• Initial release	

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 Advance: This data sheet contains initial descriptions of products still under development.