

1/4.5-Inch 1.6Mp CMOS Digital Image Sensor Addendum

MT9M002 – Serial LVDS

Refer to the MT9M002 data sheet at www.aplina.com

Introduction

This document supplements Aptina's MT9M002 advance data sheet (Revision A, dated 10/06) with updates to Table 10, Serial LVDS. The standard CMOS image sensor data sheet should be referenced for a complete description of this 1/4.5-inch 1.6Mp image sensor. The specifications contained in this addendum supersede the specifications listed in the referenced CMOS image sensor data sheet.

Table 1: Serial LVDS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V _{OH}	Output voltage HIGH, V _{OA} or V _{OB}	R _{LOAD} = 100±1%	1.2	1.3	1.6	V
V _{OL}	Output voltage LOW, V _{OA} or V _{OB}	R _{LOAD} = 100±1%	0.9	1.1	1.3	V
V _{OD}	Output differential voltage	R _{LOAD} = 100±1%	100	200	350	mV
V _{OS}	Output offset voltage	R _{LOAD} = 100±1%	1.1	1.2	1.4	V
ΔV _{OD}	Change in V _{OD} between "0" and "1"	R _{LOAD} = 100±1%			30	mV
ΔV _{OS}	Change in V _{OS} between "0" and "1"	R _{LOAD} = 100±1%			50	mV
I _{SA} , I _{SB}	Output current	Driver shorted to ground			12.6	mA
I _{SAB}	Output current	Driver shorted to ground			6.1	mA
ΔR _O	R _o mismatch between A & B	V _{CM} = 1.0V and 1.4V			10	%



Revision History

Rev. C5/2/11

- Updated trademarks
- Applied updated template

Rev. B4/10

- Updated to Aptina template
- Updated to non-confidentiala

Rev. A11/06

- Initial release

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 Advance: This data sheet contains initial descriptions of products still under development.